



**REPUBLIC OF KENYA**

**NATIONAL OCCUPATIONAL STANDARDS**

**FOR**

**ICT TECHNICIAN**

**KNQF LEVEL 5**

**PROGRAMME ISCED CODE: 061 2454A**

## APPLY BASIC ELECTRONICS

UNIT CODE: 0714 441 04A

### UNIT DESCRIPTION

This unit specifies the competencies required to apply basic electronic. It involves identifying electric circuits, identifying electronic components, applying semi-conductor theory, and classifying computer memory, applying logic gates, applying logic gates and performing Boolean algebra operations.

### ELEMENTS AND PERFORMANCE CRITERIA

ELEMENT	PERFORMANCE CRITERIA <i>(Bold and italicized terms are elaborated in the Range)</i>
1. Identify electrical circuits	1.1 Electrical circuit are identified as per electrical engineering principles
	1.2 <b>Electrical quantities and their S.I units</b> are identified as per electrical engineering standards
	1.3 <b>Types of electrical circuits</b> are identified as per electrical engineering standards
2. Identify electronic components	2.1 Electronic components are Identified as per electrical engineering standard
	2.2 Characteristic of electronic components are identified as per their operations
	2.3 Application of electronic components are Identified as per workplace functions
	2.4 Characteristics of integrated circuit are identified as per the standard mode of operations.
3. Apply Semi-conductor theory	3.1 Explanation of semiconductor theory is done as per the electronics principles.

ELEMENT	PERFORMANCE CRITERIA  <i>(Bold and italicized terms are elaborated in the Range)</i>
	3.2 Structure of matter is described as per electronics principles
	3.3 Electrons in conductors and semiconductors are explained as per electronics principles
	3.4 Types of semiconductor materials are identified as per electronics principles
	3.5 P-type and N-type materials are explained as per electronics principles
	3.6 Description of P-N junction diodes operations is done as per their operations
	3.7 Types and operations of transistors are identified as per electronics principles
	3.8 Semiconductor theory is applied in electrical circuits as per electronics principles
4. Classify computer memory	4.1 <b><i>Classification of computer memories</i></b> are identified as per their characteristics
	4.2 Memory hierarchy is identified as per memory speed
	4.3 <b><i>Levels of memory storage</i></b> are identified as per technology used.
	4.4 Classification of memories is done as per the technology used
5. Apply logic gates	5.1 Logic gates are identified as per the Digital Electronics principles
	5.2 Logic circuits are developed as per the standard procedures
	5.3 Logic circuits are simplified as per the standard procedures

ELEMENT	PERFORMANCE CRITERIA  <i>(Bold and italicized terms are elaborated in the Range)</i>
	5.4 Apply logic gates in electronic circuits as per digital Electronics principles
6. Perform Boolean algebra operations	6.1 Key concepts in Boolean algebra are explained as per the digital electronics principles
	6.2 Boolean Expressions are demonstrated as per the SOPs
	6.3 Basic Boolean operations are performed as per the SOPs
	6.4 Methods of simplifying Boolean expressions are illustrated as per the SOPs
	6.5 Boolean Laws and Theorems are illustrated as per the SOPs
	6.6 Simplification rules for Boolean expressions are illustrated as per the SOPs

## RANGE

This section provides work environments and conditions to which the performance criteria apply. It allows for different work environments and situations that will affect performance.

Variable	Range  <i>May include but is not limited to:</i>
1. Electrical quantities and their units	<ul style="list-style-type: none"> <li>● E.M.F in volts</li> <li>● Power in watts</li> <li>● Energy in joules</li> <li>● Resistance in ohms</li> <li>● Current in amperes</li> </ul>
2. Types of electrical circuits	<ul style="list-style-type: none"> <li>● AC – Alternating Current</li> </ul>

Variable	Range <i>May include but is not limited to:</i>
	<ul style="list-style-type: none"> <li>DC – Direct Current</li> </ul>
3. Types and operations of transistors	<ul style="list-style-type: none"> <li>Types <ul style="list-style-type: none"> <li>✓ PNP</li> <li>✓ NPN</li> </ul> </li> <li>Operations <ul style="list-style-type: none"> <li>✓ Forward biasing</li> <li>✓ Reverse Biasing</li> </ul> </li> </ul>
4. Types of memories	<ul style="list-style-type: none"> <li>Semi-conductor</li> <li>Magnetic</li> <li>Optical</li> </ul>
5. Classification of memories	<ul style="list-style-type: none"> <li>RAM</li> <li>ROM</li> </ul>
6. Levels of memory storage	<ul style="list-style-type: none"> <li>Internal</li> <li>Main</li> <li>Online</li> <li>Offline bulk</li> </ul>

## REQUIRED KNOWLEDGE AND UNDERSTANDING

The individual needs to apply knowledge and understanding of:

- Electrical Components
- Electrical Quantities and units of measurement
- Electrical circuits
- Semiconductor theory

- Types of Computer memories
- Boolean algebra
- Logic gates

## FOUNDATION SKILLS

The individual needs to apply the following foundation skills:

- Communications (verbal and written);
- Proficient in ICT
- Time management
- Problem solving
- Decision making
- First aid

## EVIDENCE GUIDE

This provides advice on assessment and must be read in conjunction with the performance criteria, required knowledge and understanding and range.

1. Critical Aspects of Competency	<p><b><i>Assessment requires evidence that the candidate:</i></b></p> <p>1.1 Identified electrical quantities and their S.I units as per electrical engineering standards</p> <p>1.2 Identified types of electrical circuits as per electrical engineering standards</p> <p>1.3 Identified electronic components as per electrical engineering standard</p> <p>1.4 Memory storage as per technology used</p> <p>1.5 Identified application of electronic components as per work place functions</p>
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	<p>1.6 Identified type and operations of transistors as per electronics principles</p> <p>1.7 Identified logic gates as per the Digital Electronics principles</p> <p>1.8 Developed logic circuits as per the standard procedures</p> <p>1.9 Simplified logic circuits as per the standard procedures</p> <p>1.10 Performed basic Boolean operations as per work procedures</p>
2. Resource Implications	<p>The following resources should be provided:</p> <p>2.1 Appropriately simulated environment where assessment can take place</p> <p>2.2 Access to relevant work environment</p> <p>2.3 Resources relevant to the proposed activities or tasks</p>
3. Methods of Assessment	<p>Competency in this unit may be assessed through:</p> <p>3.1 Practical</p> <p>3.2 Projects</p> <p>3.3 Third party reports</p> <p>3.4 Portfolio of evidence evaluation</p> <p>3.5 Written tests</p>
4. Context of Assessment	<p>This Competency may be assessed individually in a workplace or simulated workplace</p>
5. Guidance information for assessment	<p>Holistic assessment with other units relevant to the industry sector, workplace and job role is recommended.</p>